

Application No. 10/087,774  
Amendment dated June 6, 2006  
Reply to Office Action of February 6, 2006

Docket No.: 0941-0418P

**AMENDMENTS TO THE DRAWINGS**

Attached to this Amendment are Replacement sheets of drawings for Figs. 2 and 3.

### **REMARKS**

Applicants thanks the Examiner for the thorough consideration given the present application. Claims 1-12 are currently being prosecuted. The Examiner is respectfully requested to reconsider his rejections in view of the amendments and remarks as set forth below.

#### **Telephone Interview**

Applicants note with appreciation the telephone conducted with Examiner Yenke on May 18, 2006. During the interview, the outstanding rejection was discussed in order to better determine how the reference is being applied. While various possibilities were discussed, no agreement was reached as to the allowability of the claims.

#### **Drawings**

The Examiner objected to the drawings as lacking labels for the various elements. By way of the present Amendment, Applicants are submitting Replacement Drawings for Figs. 2 and 3 which include these labels. Accordingly, Applicants submit that this objection is overcome.

#### **Rejection Under 35 USC 102**

Claims 1-3 and 8-11 stand rejected under 35 USC 102 as being anticipated by Cottle et al. (U.S. Patent 6,263,396). This rejection is respectfully traversed.

The Examiner relies on Fig. 18e of the reference to show a frame memory 312, a memory 240 and circuitry for copying the OSD data into the display memory 312 from the memory 240. Applicants submit that claim 1 is not anticipated by this reference.

Claim 1 recites an OSD control method comprising storing frame data, which corresponds to a frame of a display screen without OSD data in the display buffer with plural registers; storing the OSD data which corresponds to an OSD window included in the frame in a memory; and copying the OSD data and storing the OSD data to the registers corresponding to the OSD window by a data processing method. Since a frame is an area in which text or graphics can appear on a display, it is known that the display buffer comprises the frame data corresponding to the frame of the display screen. Thus, the frame data stored in the registers corresponding to the OSD window will be replaced by the OSD data when the OSD data is stored in the registers corresponding to the OSD window of the display buffer.

Cottle et al. does not teach or suggest the step of storing frame data, which corresponds to a frame load display screen without OSD data in a display buffer with plural registers. Cottle et al. recites that the display area that does not contain any OSD data, such as background color or motion video, is not included in the frame memory (Col. 42, lines 37-39). Thus, data stored in the frame memory of Cottle et al. is OSD data only, not "frame data corresponding to a frame load display screen without OSD data."

The Examiner believes that Cottle et al. discloses that the display screen may include non-OSD data (full screen background 1110 or full screen MPEG motion picture 1120) in addition to OSD data (windows 1100, 1102, 1104, 1106, 1122, 1124). Thus, the frame data corresponds to the frame of the display screen with and/or without OSD data to meet the claimed limitation.

Applicants disagree that Cottle et al. teaches or suggests a storing frame data corresponding to a frame of a display screen without OSD data in a display buffer with plural registers. Fig. 18d of the reference shows a display result, such as the images displayed on

display screen 60 of Figs. 2 and 3 of the present invention. Thus, Cottle et al. does not teach or suggest the display result being generated by storing frame data corresponding to a frame of a display screen without OSD data in a display buffer with plural registers and copying OSD data and storing the OSD data to the registers corresponding to the OSD window by a data processing method. Accordingly, Applicants submit that claim 1 is allowable.

Claims 2-11 depend from claim 1 and as such are also considered to be allowable. In addition, each of these claims recite other features which make them additionally allowable.

#### Rejection Under 35 USC 103

Claims 4-7 stand rejected under 35 USC 103 as being obvious over Cottle et al. This rejection is respectfully traversed.

The Examiner feels that it would have been obvious to provide a transfer flag via the bit block transfer mode or an overlay flag. Applicants submit that even if it would be obvious to provide these flags, these claims remain allowable based on their dependency from allowable independent claims.

Applicants have also added new claim 12 which states that the OSD data replaces the frame data stored in the register corresponding to the OSD window. Since Cottle et al. does not teach or suggest storing frame data corresponding to a frame of a display screen without OSD data in a display buffer with plural registers, Applicants submit that it is clear that the OSD data replacing the frame data stored in the register corresponding to the OSD window is not taught by the reference. Accordingly, Applicants submit that claim 12 is further allowable.

Conclusion

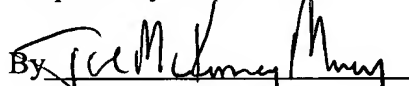
In view of the above remarks, it is believed that the claims clearly distinguish over the patent relied on by the Examiner. In view of this, reconsideration of the rejection and allowance of all of the claims are respectfully requested.

If the Examiner has any questions or comments, please contact Robert F. Gnuse, Reg. No. 27,295 at the offices of Birch, Stewart, Kolasch & Birch, LLP.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

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Respectfully submitted,

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Attachments: Replacement Sheets for Figs. 2 and 3